# 程序烧录指导书

基于win7 64位旗舰版操作系统 2015/05/25















● 1.0 打开'1.烧录程序'文件夹,再打开'驱动与程序'文 件夹,将里面'Setup JLinkARM V428c'解压,得到安装程序。 双击安装程序,弹出对话框,点击'YES',继续点'NEXT', 到'Choose options'的时候勾选'Add shortcuts to desktop'(如图),一直'NEXT'到最后'Finish'。安装完毕 如图所示。

| 😼 Choose options | A10.00 A10   |        | 😼 Installation Complet | te                           |
|------------------|--|--------|------------------------|------------------------------|
|                  | Choose options for creating shortcuts  |        |                        | J-Link ARM V4.34 has bee     |
|                  | <ul> <li>Create entry in start menu</li> <li>Add shortcuts to desktop</li> </ul> |        |                        | Press the Finish button to e |
|                  | < <u>B</u> ack <u>N</u> ext >  | Cancel |                        | < <u>B</u> ack               |

- X n successfully installed. kit this installation. Finish > Cancel



● 1.1把J-1ink的usb端连接至计算的usb端,右键'计算 机',选择'属性',选择'设备管理器',单击'通用 串行总线控制器',我们可以看到'J-Link driver',如 图所示,则表示J-LINK软件安装成功。

| 文件(E) 操作(A) 查看(V) 帮助(H)<br>◆ ● ○ II A ANDY-PC<br>● ○ IDE ATA/ATAPI 控制器<br>● ④ UBE TA/ATAPI 控制器<br>● ④ UBE TA/ATAPI 控制器<br>● ④ UBE TA/ATAPI 控制器<br>● ④ UBE TA/ATAPI 控制器<br>● ④ USE TA/ATAPI 控制器<br>● ④ USB Koot Hub<br>● ③ Generic USB Hub<br>● ④ USB Koot Hub<br>● ③ USB Koot Hub<br>● ④ USB Koot Hub<br>● ③ USB Koot Hub   |  | _ 0 |
|---|--|-----|
| <ul> <li>◆ ● ● □ ● □ ● ■ ●</li> <li>● □ DE ATA/ATAPI 控制器</li> <li>● □ 使携设备</li> <li>● ● ● ● ● ● ● ● ● ● ● ● ● ● ● ● ● ● ●</li></ul>   | 文件(F) 操作(A) 查看(V) 帮助(H)  |     |
| <ul> <li>▲ ANDY-PC</li> <li>□ IDE ATA/ATAPI 控制器</li> <li>□ 便携设备</li> <li>□ 欧温驱动器</li> <li>□ 欧温驱动器</li> <li>□ 欧温驱动器</li> <li>□ 欧温驱动器</li> <li>□ 读 计算机</li> <li>□ 医视器</li> <li>□ 读 出现器</li> <li>□ 读 医eneric USB Hub</li> <li>□ ↓ Link driver</li> <li>□ Generic USB Hub</li> <li>□ ↓ USB Host Controller</li> <li>□ ↓ USB Composite Device</li> <li>□ ↓ USB Composite Device</li> <li>□ ↓ USB Root Hub</li> <li>□ ↓ USB Root Hub</li> <li>□ ↓ USB Atog Light Standard Enhanced PCI to USB Host Controller</li> <li>□ ↓ USB Root Hub</li> <li>□ ↓ USB Atog Light Standard Enhanced PCI to USB Host Controller</li> </ul> |  |     |
| ₩ ¥ H / (K) USB 5.0 使来或論  | ANDY-PC     IDE ATA/ATAPI 控制器     IDE ATA/ATAPI 在自動器     IDE ATA/ATAPI 在自動和IDE ATA/ATAPI 在自動和IDE ATA/ATAPI 和IDE ATA/ATAPI ATA/ATA/ATAPI ATA/ATAPI ATA/ATA/ATA/ATA/ATA/ATA/ATA/ATA/ATA/ATA |     |
|   |  |     |







● 2.0 打开'驱动与程序'文件夹, 解压'CDM 2.08.30 WHQL Certified 64' 文件,得到'CDM v2.08.30 WHQL Certified'应用程序如图(已标出),双击该应用程序然 后跳出对话框选中'EXTRACT',点击'下一步',再点击 '完成',安装完毕。

| 1. A. C. M.       |                                  |                 |             |          |   |  |  |  |  |
|-------------------|----------------------------------|-----------------|-------------|----------|---|--|--|--|--|
| ○○ - ↓ 11 +       |                                  |                 |             |          | • |  |  |  |  |
| 文件(F) 编辑(E) 查看(V) | 工具(T) 帮助(H)                      |                 |             |          |   |  |  |  |  |
| 组织 ▼   包含到库中 ▼    | 共享 ▼ 新建文件夹                       |                 |             |          |   |  |  |  |  |
| ☆ 收藏夹             | 名称                               | 修改日期            | 类型          | 大小       |   |  |  |  |  |
| 🚺 下载              | ]) CDM 2.08.30 WHQL Certified_64 | 2014/3/20 14:50 | 文件夹         |          |   |  |  |  |  |
| 📃 桌面              | 🗐 CDM 2 08 30 Release Info       | 2013/7/29 15:21 | RTF 格式      | 188 KB   |   |  |  |  |  |
| 週 最近访问的位置         | CDM v2.08.30 WHQL Certified      | 2013/7/29 15:13 | 应用程序        | 1,887 KB |   |  |  |  |  |
|                   | 💼 CDM v2.08.30 WHQL Certified    | 2013/7/29 15:18 | 好压 ZIP 压缩文件 | 1,338 KB |   |  |  |  |  |
| 🍃 库               |                                  |                 |             |          |   |  |  |  |  |
| 🛃 视频              |                                  |                 |             |          |   |  |  |  |  |
| ▶ 图片              |                                  |                 |             |          |   |  |  |  |  |
| 🗟 文档              |                                  |                 |             |          |   |  |  |  |  |
| ⊿) 音乐             |                                  |                 |             |          |   |  |  |  |  |
|                   |                                  |                 |             |          |   |  |  |  |  |
| 🖳 计算机             |                                  |                 |             |          |   |  |  |  |  |
| 🏭 本地磁盘 (C:)       |                                  |                 |             |          |   |  |  |  |  |
| - D (D·)          |                                  |                 |             |          |   |  |  |  |  |





2.1 把开发板连接到电脑usb,led亮起红灯,(未亮则把标示出来的'开关'往外拨。直到红灯正常)如图所示,然后如步骤2.0打开'设备管理器'点击'端口(COM和LPT)',查看有'USB Serial Port(COMX)'X代表数字,不同电脑X不一样,如图所示,则代表此驱动安装ok,记住'COMX'这个端口,之后会用到。





● 3.0检测软件安装 此时usb转TTL还与电脑相连, 打开"超级 终端1.0" 文件夹,双击"hypertrm.exe"应用程序,如下图, 在"名称"一栏填写CM1,然后确定,跳至下一个对话框,在 "连接时使用"一栏选择上一步看到的COM口,我的是COM1就选 择COM1.

| ■ 新建连接 - 超级终端  | <u> </u> | CM1 - 超级终端     CM1 - 超级终端           |
|--|----------|-------------------------------------|
| 文件(F) 编辑(E) 查看(V) 吁叫(C) 传送(T) 帮助(H)  |          | 文件(F) 编辑(E) 查看(V) 呼叫(C) 传送(T) 帮助(H) |
| 🗋 🖉 🗿 💲 🛍 🔁 😭  |          | D 📽 🎯 💲 🗅 🗃 📸                       |
| 海線描述     新建连接       新建连接     輸入名称并为该连接选择图标:       名称 ⑪):     四标 ①:       一     四       確定     職定 |          |                                     |
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3.1然后选择"确定"在"位/秒(B)"中选择"115200"即可, 点击"确定",退出的时候选择关闭对话框,选择"断开连接", 保存该文件。在"数据流控制"一栏中选择"无"。

| COM1 Et                           | CM1- 超级终端                                  |
|-----------------------------------|--|
| 横口设置                              | 文件(F) 編載(E) 査羃(V) 盱叫(C) 検送(T) 報助(H)        |
|                                   |  |
| 2/₩ Φ): 115200 •                  |  |
| 教授位 ①: 8                          |  |
| 奇偶极验 (E): 无                       |  |
| 傳止位 (D): 1 · · · ·                |  |
| 教掘廃控制(犯):   健件 ・                  |  |
| 还原为假认值(8)                         |  |
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|                                   | 已连接 0:00:25 自动检测 自动检测 SCROLL CAPS NUM 排 打印 |
| 新开 目动检测 目动检测 SCROLL CAPS NUM 语 打印 |  |





# 3.2下次启动的时候,直接关闭"连接描述",打开文件夹如 图,加载CM1就行,如图所示。



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| ) 通讯 ) 超级终端 | • 47 | 搜索超级终端     | -     | ٩ |
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| 类型          | 大小   |            |       |   |
| 23 HT 文件    | 2 KB |            |       |   |
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### ● 4.0 至此2个驱动软件已安装完毕。





● 1.0开发板与工装连接,注意:接线时从夹具的后面接线(如 图)。开发板的放置方向(平行连接),分别连接好四个开发板。 (详细介绍连接方式)线不能接错,必须一对一,再用万用表测 试是否接触良好。







● 2.0把J-1ink上的排线与开发板排线插槽连接好(注意 方向);开发板的usb连接好并和j-link的usb分别连接到 电脑上,如图所示。打开开发板上开关,红色1ed灯会亮起。







 1.0 打开桌面快捷方式'J-Flash ARM V4.28c'把'1. 烧录程序'文件夹中的'ALINK\_KELON\_1062\_AC@016'(该 程序为需要烧录的1M bin文件)摁住左键不放拖入到'J-Flash ARM V4.28c'的工作界面,则会提示'start address',填写800000如图,选择'0K'。

| File       Edit       View       Target       Options       Window       Help         Image: Project - Def       Image: Project - |
|---|
| Name     Value       Connection     USB [Device 0]       Target interface     JTAG  |
| Init JTAG speed       5 kHz         JTAG speed       Auto recognition         TAP number <not used="">         IRPre       <not used="">         IRPre       <not used="">         CPU       Auto         Endian       Little         Check core Id       No         Core Id       0x0         Use target RAM       No         RAM address       0x0         Base address       0x0         Organization       16 bits x 1 chip         If labits x 1 chip       Cancel</not></not></not>   |
| Application log started<br>- J-Flash AKM V4.28c (J-Flash compiled Jul 1 2011 14:23:45)<br>- JLinkARM. dll V4.28c (DLL compiled Jul 1 2011 14:23:12)<br>Reading flash devices Itat [D:\Program Files\JLinkARM_V428c\ETC\JFlash\Flash.csv]<br>- List of flash devices read successfully (884 Devices)<br>Reading MCU device list [D:\Program Files\JLinkARM_V428c\ETC\JFlash\MCU.csv]<br>- List of fluct devices read successfully (933 Devices)<br>Creating new project file [D:\Program Files\JLinkARM_V428c\Default.jflash]<br>- New project created successfully<br>Opening data file [C:\Users\ANDY\Desktop\Hisense test\foxxconn_otp_3162_SPI_1M.bin]   |
|   |



● 2.0选择'OK'后会出现如下图所示,选择最小化窗口。 已红色框出。然后键盘'ALT+F7'组合键,跳出'Project settings',选择'Target Interface'设置如下图

| 🔜 SEGGER J-Flash ARM V4.28c - [D:\Program Files\JLink   | nkARM_V428c\Default.jflash *]   | Project settings                              |
|---|---|---|
| File Edit View Target Options Window Help   |   |   |
| Rame Value  | NDY\Desktop\Hisense test\foxxconn_otp_3162_SPI_1M.bin   | General Target Interface CPU Flash Production |
| Connection USB [Device 0]   |   | ISMD T  |
| Target interface JTAG Address Ø   | 0 1 2 3 4 5 6 7 8 9 A B C D E F ASCII   | SWD speed before init                         |
| Init JTAG speed 5 kHz   | D0 65 01 20 9D 01 00 08 83 0F 00 20 69 0A 00 20 .e i  |   |
| JTAG speed Auto recognition   | 81 0F 00 20 F5 06 00 20 75 18 00 20 00 00 00 00 u<br>00 00 00 00 00 00 00 00 00 00 00 00 91 11 00 20  | (     Auto selection     I     Auto selection |
| IRPre <not used=""></not>   | F7 06 00 20 00 00 00 00 F9 0F 00 20 F5 14 00 20   | O 5 ▼ kHz C 4000 ▼ kH                         |
| 8000040 B   | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| CPU Auto B000050 B  | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| Check core Id No 8000060 B  | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| Core Id 0x0 8000070 B:  | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| RAM address 0x0   | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| RAM size 4 KB 800090 B  | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| Flash memory Auto detection   | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| Base address 0x0  | B3 01 00 08 09 15 00 20 B3 01 00 08 B3 01 00 08   |   |
| Organization 16 bits x 1 chip 8000000 B.  | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| 80000F0 B   | R3 01 00 08 R3 01 00 08 R3 01 00 08 R3 01 00 08   |   |
| 80000F0 B   | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| 8000100 B   | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| 8000110 B   | B3 01 00 08 B3 01 00 08 B3 01 00 08 B3 01 00 08   |   |
| R000120 R   | R3 01 00 08 R3 01 00 08 R3 01 00 08 R3 01 00 08 🔳 🔳   |   |
| LOG   |   |   |
| Application log started<br>- J-Flash AKM V4.28c (J-Flash compiled Jul 1 2011 1<br>- JLinkARM. dll V4.28c (DLL compiled Jul 1 2011 14:2<br>Reading flash device list [D:\Program Files\JLinkARM_4<br>- List of flash devices read successfully (884 Device<br>Reading MCU device list [D:\Program Files\JLinkARM_V4<br>- List of MCU devices read successfully (933 Devices<br>Creating new project file [D:\Program Files\JLinkARM_4<br>- New project created successfully<br>Opening data file [C:\Users\ANDY\Desktop\Hisense test<br>- Data file opened successfully (1048576 bytes, 1 ra | 14:23:45)<br>(23:12)<br>(_V428c\ETC\JFlash\Flash.csv]<br>ices)<br>/428c\ETC\JFlash\MCV.csv]<br>as)<br>M_V428c\Default.jflash]<br>st\foxxconn_otp_3162_SFI_1M.bin]<br>range, CRC = 0x00000000) |   |
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|     | <br>      |  |
| 确定  | <br>应用(A) |  |



# 3.0点击'CPU'一栏选择如下图,在Device一栏选择 'ST STM32F205RG',再点击'Production'一栏中勾选如 图所示红框已标出。之后选择'确定'。完成设置。

| Project settings  | Project settings   |
|---|--|
| General Target Interface       CPU       Flash       Production         Use J-Link script file          © Device       ST STM32F205RG       Image: Check core ID         ID       4BA00477         ID       4BA00477         Mask       FFFFFFFF         Little endian       Image: Use target RAM (faster)         Addr       20000000       96 KB | General       Target Interface       CPU       Flash       Production         Production mode       Voltage threshold       3000       mV         Voltage threshold       3000       mV         Delay before start       500       ms         Program serial number       Address       00000000 |
| # Action       Value0       Value1       Comment         0       Reset       0       0 ms       Reset and halt target   | <ul> <li>Erase</li> <li>Program</li> <li>Verify</li> <li>CRC</li> <li>Secure chip</li> <li>Start application</li> </ul>  |
| Add     Insert     Delete     Edit     Up     Down       備定     取消     应用 (A)   |  |





Ready

### ● 4.0至此则烧录程序开始,直接键盘快捷键'F7'或者 在'Traget'目录下选择'AUTO'。则开始烧录程序,如 图所示,烧录完成会提示finish。点击ok即可。

| SEGGER J-Flag    | sh ARM V4.28c - [D:\P  | rogra  | am Files\JL  | inkA      | RM_    | V428      | Bc\D  | efaul      | t.jfla    | sh *] |      |            |      |      |      |            |     |    |    |     |         | _       |       |
|------------------|------------------------|--------|--------------|-----------|--------|-----------|-------|------------|-----------|-------|------|------------|------|------|------|------------|-----|----|----|-----|---------|---------|-------|
| File Edit Viev   | v Target Options       | Win    | dow Helj     | р         |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| Project - De     | ef 🗖 🗖 🔀               |        | C:\Users\    |           | D/YC   | eskto     | op\H  | lisen      | se te     | st\fo | ххсо | nn_o       | tp_3 | 162_ | SPI_ | 1M.Ł       | oin |    |    |     |         |         |       |
| Name             | Value                  |        | ddress: 0x80 | וחחחר     | 10     | x1        | ×2    | ×4         |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| Connection       | USB [Device 0]         |        |              |           |        | 1.07      |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| Target interface | SWD                    |        | Address      | 0         | 1      | 2         | 3     | 4          | 5         | 6     | 7    | 8          | 9    | A    | B    | C          | D   | E  | F  | ASC | II      |         |       |
| -                |                        |        | 8000000      | ри        | 65     | <b>Ø1</b> | 20    | 9D         | <b>Ø1</b> | QQ    | 08   | 83         | ØF   | QQ   | 20   | 69         | ØA  | 00 | 20 | e   |         |         | i     |
| Init JTAG speed  | Auto recognition       |        | 0000010      | 04        | OP.    | 00        | 20    | DE         | 96        | 00    | 20   | 70         | 10   | 00   | 20   | 00         | 00  | 00 | 00 |     |         |         |       |
| JTAG speed       | Auto recognition       |        | 8000010      | 81        | ØF     | 99        | 20    | гs         | 90        | 99    | 20   | 75         | 19   | 99   | 20   | 99         | 99  | 99 | 99 |     |         | ս       | • • • |
| TAP number       | <not used=""></not>    |        | 8000020      | 00        | 00     | 00        | 00    | 00         | 00        | 00    | 00   | 00         | 00   | 00   | 00   | 81         | 11  | 00 | 20 |     |         |         |       |
| IRPre            | <not used=""></not>    |        | 8000030      | F7        | 06     | 00        | 20    | 00         | 00        | 00    | 00   | E9         | ØF   | 00   | 20   | B5         | 14  | 00 | 20 |     |         |         |       |
|                  |                        |        | 8000040      | 83        | Ø1     | ØØ        | Ø8    | <b>B3</b>  | Ø1        | ØØ    | Ø8   | <b>B3</b>  | Ø1   | ØØ   | 08   | <b>B</b> 3 | Ø1  | аа | 08 |     |         |         |       |
| MCU              | ST STM32F205RG         |        |              | 100       | 04     | 00        | 00    | D0         | 04        | 00    | 00   | D0         | 04   | 00   | 00   | D0<br>D0   | 04  | 00 | 00 |     |         |         |       |
| Endian           | Little                 |        | 8000050      | 83        | ЮT     | 99        | 68    | 83         | ЮT        | 99    | 68   | 83         | ЮT   | 99   | 68   | 83         | ЮT  | 99 | 68 | ••• | • • • • | • • • • |       |
| Check core Id    | Yes                    |        | 8000060      | <b>B3</b> | 01     | 00        | 08    | <b>B</b> 3 | 01        | 00    | 08   | <b>B</b> 3 | 01   | 00   | 08   | <b>B</b> 3 | 01  | 00 | 08 |     |         |         |       |
| Core Id          | 0x4BA00477             |        | 8000020      | <b>B3</b> | Ø1     | <u>aa</u> | ØR    | <b>B3</b>  | Ø1        | ЮЙ    | ØR   | <b>B3</b>  | Ø1   | ØØ   | ØR   | <b>B3</b>  | 01  | 00 | 08 |     |         |         |       |
| Use target RAM   | Yes                    |        | J-Elash      |           |        |           | -     | -          | -         | -     | -    |            |      |      | 2    | 3          | 01  | ØØ | Øß |     |         |         |       |
| RAM address      | 0x20000000             | IН     |              | -         | -      | _         | _     | -          | -         | _     | -    | _          | -    | _    | _    |            | 04  | 00 | 00 |     | ••••    |         |       |
| HAM size         | 96 KB                  | IН     | Programm     | nina ir   | n fast | mode      | 1     |            |           |       |      |            |      |      |      |            | 10T | 99 | 68 |     | • • • • |         |       |
|                  |                        |        | With use     | of R      | AM)    | mode      |       |            |           |       |      |            |      |      |      |            | 01  | 00 | 08 |     |         |         |       |
| Flash memory     | STM32F20xxG internal   |        | D            |           | a      |           | 0000  | 11700      |           |       |      |            |      |      |      |            | 01  | 00 | 08 |     |         |         |       |
| Manuracturer     | 51<br>1004 KD          |        | Frogram      | ning (    | ළ අපර  | liess     | υχυδι | лисэ       | 0%        |       |      |            |      |      |      |            | Ø1  | аа | 08 |     |         |         |       |
| Size             | 1024 NB                | IН     |              |           |        |           |       |            | 3%        |       |      |            |      |      |      | -          | 01  | 00 | 00 |     |         |         |       |
| Flash Id         | UXU<br>No              | IН     |              |           |        |           |       |            |           |       |      |            |      |      |      |            | DI  | 99 | 69 |     | • • • • |         |       |
| Race address     |                        |        |              |           |        |           | E     |            |           | _     |      |            |      |      |      |            | 01  | 00 | 08 |     |         |         |       |
| Dase address     | 22 bits v 1 obin       |        |              |           |        |           |       | C          | ancel     | - 1   |      |            |      |      |      |            | 01  | 00 | 08 |     |         |         |       |
| organization     | 52 bits A F Grip       |        |              |           |        |           |       |            |           |       |      |            |      |      |      |            | Ø1  | ро | 08 |     |         |         |       |
|                  |                        |        | 0000110      | DO        | 01     | 00        | 00    | <b>D</b> 2 | 01        | 00    | 00   | <b>D</b> 2 | 01   | 00   | 60   | 02         | 01  | 00 | 60 |     |         |         |       |
| 4                | 4                      |        | 2000110      | 83        | 01     | 99        | 00    | <b>B</b> 3 | OT.       | 99    | 60   | 63         | OT.  | 99   | 00   | 63         | OT. | 99 | 80 |     | • • • • |         |       |
|                  |                        |        | 2000120      | 83        | 611    | 66        | ΩŊ    | R.S        | 611       | 66    | ØŊ   | 83         | 611  | 66   | ßŊ   | ВЗ         | 611 | 66 | ØŊ |     |         |         |       |
|                  |                        |        |              |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| HASH LOG         |                        |        |              |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| Opening data fi  | le [C:\Users\ANDY\Des  | sktop  | \Hisense to  | est\;     | foxxc  | onn_      | otp_3 | 3162_      | SPI_      | 1М.Ъі | n] . |            |      |      |      |            |     |    |    |     |         |         |       |
| - Data file op   | ened successfully (10  | J4857I | 6 bytes, 1   | rang      | ge, U  | :RC =     | UxUU  | JUUUU      | 00)       |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| - Connecting     | ig carget (1040010 by) | ces,   | I fange)     |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| - Connected      | successfully           |        |              |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| - Unsecure chi   | p                      |        |              |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| - Chip unse      | cured successfully     |        |              |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| - Erasing affe   | ected sectors          | 6 1    | 0 11         |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| - Erase ope      | eration completed succ | cessf  | ully         |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
| - Programming    | target (1048576 bytes  | s, 1 : | range)       |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |
|                  |                        |        |              |           |        |           |       |            |           |       |      |            |      |      |      |            |     |    |    |     |         |         |       |

Connected







### 5.0或许会出现烧录失败的情况。此时可以尝试断电, 重新使j-link和开发板与电脑连接。或者重启'J-Flash ARM V4.28c'软件。





### 结束烧录之后必须全部进行测试,防止漏烧,有需要可以记录MAC地址 等



### 1.0 首先设置开发板,如下图所示, "BOOT"开关为"L"将 "STATUS"开关拨至"L"档, "STANDBY"和"SLEEP"开关为 "H"。





# 2.0打开测试软件,超级终端,即我们在软件安装已经设置好的那个软件。然后放置测试模块如图所示。

| 1  | CM1   | ·超级终期    |       |         |       |                  |  |   |
|----|-------|----------|-------|---------|-------|------------------|--|---|
| 文  | 4(F)  | 编辑(E)    | 查看(V) | (C) (C) | 传送(T) | 帮助(H)            |  |   |
| ۵  | È     | 9 🏅      | -C 🎝  | f       |       |                  |  |   |
| 6  |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  | Ξ |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
|    |       |          |       |         |       |                  |  |   |
| L  |       |          |       |         |       |                  |  |   |
| Bi | 接 0.5 | 01:05 自計 | 対合別   | 自动检测    | SCR   | LL CAPS NUM 捕 打印 |  |   |





3.0把开发板开关打开,摁下开发板上的RESET键(有时候可以 不用,直接回跳出测试结果),红圈以表示出来。并记录该MAC 地址。(Version是烧录版本, MAC后面的是地址)记录好了之后, 在工装上更换下一模块就行。这一步可以判断版本是否正确,烧 录程序地址是否正确。减小问题的发生概率。







### 4.0 检测wifi信号

在检测MAC地址的窗口中,输入ssid+空格 (注意大小写) 然后enter就可以查看周围wifi热点,如下。SSID后为AP名称, RSSL后为信号强度。(前提是固件中必须先写入了该代码或者自 动跳出)

MXCHIP\_MFMODE> ssid Scan AP Success: SSID: MXCHIP-811-2, RSSI: 100 SSID: MXHCIP-811-1, RSSI: 35 SSID: MXCHIP Guest, RSSI: 52 SSID: TP-LINK 9CF4, RSSI: 32 SSID: Winpal-Office, RSSI: 35 SSID: 360WiFi-0015, RSSI: 0 SSID: NETGEAR, RSSI: 12 SSID: MXCHIP 41C6D4, RSSI: 40









